

Electronic Acknowledgement Receipt

EFS ID:	3383392
Application Number:	10779904
International Application Number:	
Confirmation Number:	9608
Title of Invention:	Method and apparatus for defect analysis of semiconductor integrated circuit
First Named Inventor/Applicant Name:	Masahiro Ishida
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Application Type:	Utility under 35 USC 111(a)

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Submitted with Payment	yes
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Payment was successfully received in RAM	\$ 810
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
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1	Request for Continued Examination (RCE)	02008071003_rce.PDF	720789 6a7a2ea5b55e8e378b32f2ab2dab9b8c f01df576	no	3
Warnings:					
Information:					
2		02008071003_RCESUBMIS S_20080530154851.pdf	181808 e26dd4e38a629e3850bebf60596069 e725a72	yes	8
	Multipart Description/PDF files in .zip description				
	Document Description		Start	End	
	Amendment Submitted/Entered with Filing of CPA/RCE		1	1	
	Claims		2	4	
	Applicant Arguments/Remarks Made in an Amendment		5	8	
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8204 891154928f890e9a546be11858b3f9109 35eca54	no	2
Warnings:					
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Total Files Size (in bytes):			910801		
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